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RESPONSE AFTER FINAL
EXPEDITED PROCEDURE
EXAMINING GROUP 2816
2800

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

SATO

Serial Number: 09/843,820

Filed: April 30, 2001

For: SEMICONDUCTOR INTEGRATED CIRCUIT HAVING CIRCUIT FOR
CHANGING TIMING OF INACTIVATING POWER-ON RESETTING CIRCUIT



Attorney Docket No: 108397-00042

Group Art Unit: 2816

Examiner: A. Luu

AMENDMENT AND RESPONSE UNDER 37 C.F.R. § 1.116

Commissioner for Patents
Washington, D.C. 20231

July 10, 2002

Sir:

In reply to the Office Action dated April 10, 2002, please amend the above-identified application as follows:

IN THE CLAIMS:

Please amend claims 1, 7 and 8 as follows:

1. (Amended) A semiconductor integrated circuit comprising:
a power-on resetting circuit for activating a reset signal in response to an initial supply of a power supply to initialize an internal circuit, and for inactivating the reset signal to a predetermined period after the initial supply to terminate an initialization of the internal circuit; and
following
a timing changing circuit for adjusting the predetermined period.

7. (Amended) A semiconductor integrated circuit comprising:
a power-on resetting circuit configured to inactivate a reset signal which initializes an internal circuit in response to a power supply being switched on, said inactivating